



Netronome Flow Processing Expert Featured at Global Semiconductor Alliance Ecosystem Summit

Santa Clara, CA – October 5, 2011 – Netronome, the leading developer of [network flow processors](#), today announced that Bruce Wilford, a Netronome Distinguished Engineer, will be among industry experts speaking at the Global Semiconductor Alliance’s Ecosystem Summit. Mr. Wilford will share insights to the best design practices easing software integration with highly programmable, multi-threaded many-core processors.

“GSA is pleased to have Netronome represented at this inaugural event,” said Jodi Shelton, president of GSA. “The Session II panel is a great forum for Netronome to contribute to the discussion on the challenges of software/hardware integration and to leverage their companies’ strengths by bringing real solutions to the semiconductor ecosystem.”

The [GSA’s Semiconductor Ecosystem Summit](#) takes place October 6 at the Santa Clara Convention Center. The one-day event focuses on three core components of the semiconductor business model - supply chain practices, technology evolution and financial trends. Distinguished executives from leading semiconductor companies will address critical topics including collaboration in the ecosystem, supply chain practices for sustainable partnerships, smart technology development, hardware/software integration and redefining the funding model.

Mr. Wilford will be participating in Session II: Advanced Technology Development. His panel will discuss “Improving Device Performance — Simplifying Software/Hardware Integration” and will take place from 1:45 p.m. to 2:45 p.m. Mr. Wilford will focus on the ideal methodology in which high-performance software is written, debugged and simulated in a cycle-accurate environment. The panel will be moderated by Richard Goering of Cadence Design Systems and additional panel participants include executives from BSQUARE, MontaVista Software and NVIDIA.

“As a leader in flow processing, Netronome provides our customers with all of the building blocks required for high-performance communications equipment designs, including the silicon, sample application software, software development tools and hardware reference platforms,” said Mr. Wilford. “Our internal developments and experiences with our broad customer base provide us a unique perspective on best practices for hardware and software integration.”

For more information about the GSA’s Semiconductor Ecosystem Summit and to register for this event, visit http://www.gsaglobal.org/events/2011/1006/registration_info.aspx.

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The Flow Processing Company™

About Netronome

[Netronome](http://www.netronome.com) is a leading developer of highly programmable semiconductor products that are used for intelligent flow processing in network and communications devices. Netronome's solutions include network flow processors and acceleration cards that scale from 10 to 200 Gbps. They are used in carrier-grade and enterprise-class communications products that require deep packet inspection, flow analysis, content processing, virtualization and security. Netronome's products are developed in labs in Santa Clara, CA, Boxborough, MA and Pittsburgh, PA. For more information on Netronome's products and technology, visit www.netronome.com.

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