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## **Netronome to Unveil Research Results on the Impacts of Stateful Flow Processing on Various Processor Architectures**

***Pioneers of flow processing technology featured in two sessions at Linley Tech Processor Conference for Networking and Communications***

**Santa Clara, CA – September 27, 2010** – Netronome, the leading developer of network flow processors, today announced that the company will disclose research results on the impacts of stateful flow processing when implemented on various processor architectures. Netronome experts will discuss the details of their findings in two sessions at the Linley Tech Processor Conference taking place September 27 – 28, 2010 at the Doubletree Hotel in San Jose, California, which focuses on processors and related technologies for networking and communications applications.

An increasing number of networking and communications applications require higher-level security processing and stateful packet inspection. The throughput, latency and high-touch computation on millions of simultaneous flows results in far more complex processing. Netronome's research shows that ordinary cache-based architectures found in multicore processors and pipelined network processors are ill-suited for future designs as networks evolve to 10, 40 and 100 Gbps.

The company's director of product management, Daniel Proch, a featured presenter at the conference, will present, "Surpassing the Bandwidth Limitations of Cache-Based Architectures," taking place during Session 5 on Tuesday, September 28 from 9:00 – 11:50 a.m. A Q&A session and panel discussion will follow his presentation.

"As network bandwidths continue to increase at exponential rates, pipeline and cache-based processing strategies are failing to support these throughputs," said Mr. Proch. "My presentation will describe why stateful flow processing is required to support IP-based services and how network flow processors are required to keep pace with increasing network speeds."

In addition, Netronome's CEO, Niel Viljoen, will participate in a panel of industry executives that will discuss the benefits and limitations of leading processors in light of complicated requirements of new communications designs. The "Multicore vs. NPU Smackdown!" panel will take place on Tuesday, September 28 from 11:50 a.m. – 12:30 p.m.

"Our research proves that stateful processing of millions of simultaneous flows has significant consequences on legacy processors," said Viljoen. "This validates the need for a new architecture purposely defined to satisfy the performance, latency, security and deep packet inspection requirements of new communications designs."

"A tidal wave of new processors has been announced in the past year, but determining which ones offer the features required by new communications systems can seem like a daunting task," said Joseph Byrne, senior analyst, The Linley Group. "The Linley Tech Processor Conference is a unique forum for networking-system engineers to discuss these most recent trends in processor design with the leading suppliers of processors and related technology. Attendees have opportunities to talk informally with

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each other and suppliers to get the personalized information they need to make decisions about their network-system designs.”

For more information about the Linley Group Tech Seminars and to register for this event, visit [http://linleygroup.com/Seminars/conference\\_fall.html](http://linleygroup.com/Seminars/conference_fall.html).

**About the Linley Group**

The Linley Group is the leading provider of independent technology analysis of semiconductors for networking, communications, mobile, and wireless. The company provides in-depth technology reports and interactive seminars. For free access to analysis of recent news subscribe to “Linley Wire” and “Linley on Mobile,” at [www.linleygroup.com/npu/wire.html](http://www.linleygroup.com/npu/wire.html).

**About Netronome**

Netronome is a leading developer of highly programmable semiconductor products that are used for intelligent flow processing in network and communications devices. Netronome’s solutions include network flow processors and acceleration cards that scale from 10 to 100 Gbps. They are used in carrier-grade and enterprise-class communications products that require deep packet inspection, flow analysis, content processing, virtualization and security. Netronome’s products are developed in labs in Santa Clara, CA, Boxborough, MA and Pittsburgh, PA. To learn more about Netronome and its products, please visit [www.netronome.com](http://www.netronome.com).

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